

WHAT IS CLAIMED IS:

1. A liquid crystal display device comprising:
 - a first substrate having a common electrode;
 - a second substrate having an active area;
 - a seal pattern formed peripherally to said active area, and between said first substrate and said second substrate;
 - a liquid crystal layer between said first substrate and said second substrate, and on the active area; and
 - an electrode pattern adjacent to said seal pattern and outside said active area.
2. The device of claim 1, wherein said electrode pattern is between said seal pattern and said second substrate.
3. The device of claim 1, wherein said second substrate comprises:
 - data lines and gate lines arranged in a matrix shape to define pixel areas;
 - a TFT at an intersection of a data line and a gate line;
 - a protective film on the TFT; and
 - a pixel electrode on said protective film.
4. The device of claim 3, wherein the electrode pattern is between said seal pattern and said protective film.
5. The device of claim 3, wherein said electrode pattern is the same material as said pixel electrode.

6. The device of claim 3, wherein said electrode pattern is formed at the same time as said pixel electrode.

7. The device of claim 1, wherein said electrode pattern forms an electric field with said common electrode.

8. The device of claim 1, wherein said electrode pattern is applied with a constant DC bias voltage.

9. The device of claim 1, wherein said electrode pattern is applied with a voltage which is stepped down from a common voltage applied to said common electrode.

10. The device of claim 1, wherein said electrode pattern is applied with a voltage which is reversed in polarity from a common voltage applied to said common electrode.

11. A liquid crystal display device comprising:

a first substrate having a common electrode;

a second substrate having an active area;

a seal pattern formed peripherally to said active area between said first substrate and said second substrate, and having projected corner portions; and

a liquid crystal layer between said first substrate and said second substrate in said active area.

12. The device of claim 11, further comprising an electrode pattern on a portion of said second substrate adjacent to said seal pattern outside of said active area.

13. The device of claim 12, wherein said electrode pattern is between said seal pattern and said second substrate.

14. The device of claim 11, wherein said second substrate comprises:
data lines and gate lines arranged in a matrix shape to define pixel areas;
a TFT at an intersection of a data line and a gate line;
a protective film on the TFT; and
a pixel electrode on said protective film.

15. The device of claim 14, wherein said electrode pattern is between said seal pattern and said protective film.

16. The device of claim 14, wherein said electrode pattern is made of the same material as said pixel electrode.

17. The device of claim 14, wherein said electrode pattern is formed at the same time as said pixel electrode.

18. The device of claim 12, wherein said electrode pattern forms an electric field with said common electrode.

19. The device of claim 12, wherein said electrode pattern is applied with a constant DC bias voltage.

20. The device of claim 12, wherein said electrode pattern is applied with a voltage which is stepped down from a common voltage applied to said common electrode.

21. The device of claim 12, wherein said electrode pattern is applied with a voltage which is reversed in polarity from a common voltage applied to said common electrode.

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